

3 a pixel unit constituted by a two-dimensional array of
4 pixels for generating charge in correspondence to received
5 light and accumulating the charge for a predetermined
6 period of time;

7 a vertical transfer unit for vertically transferring
8 charge from the pixels in the pixel unit, a horizontal
9 transfer unit for horizontally transferring charge from the
10 vertical transfer unit;

11 shift gates each provided between each pixel and the
12 vertical transfer unit for reading out the charge in the
13 pixels to the vertical transfer unit, gate electrodes for
14 controlling the shift gates; and

15 a plurality of lead lines and a plurality of
16 connection terminals for connecting the gate electrodes to
17 an external circuit,

18 the gate electrodes making up N of gate electrode
19 groups in which the lines belonging to each coset of
20 modulo N within successive pixel rows are connected to
21 common lead lines, N being a predetermined natural number
22 between 4 and one half the number of pixels in a column,
23 and also being a minimum number corresponding to a periodic
24 unit of gate electrode connections to said connection
25 terminals within said successive pixel rows, the gate
26 electrodes having common connection terminals to reduce the
27 number of the connection terminals to less than N.

Please replace claim 2 with the following:

1 2. (TWICE AMENDED) A solid-state imaging device
2 comprising:

3 a pixel unit constituted by a two-dimensional array of
4 pixels for generating charge in correspondence to received